

***IN THE UNITED STATES PATENT AND TRADEMARK OFFICE***

Applicant: David CASEY

Title: BIPOLAR TRANSISTOR WITH  
A LOW SATURATION  
VOLTAGE

Appl. No.: 10/566,813

International Filing Date: 7/12/2004  
371(c) Date: 1/30/06

Examiner: Jae Lee

Art Unit: 2895

Confirmation Number: 8053

**ISSUE FEE TRANSMITTAL**

Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Enclosed herewith please find Issue Fee Transmittal Form PTOL-85(B).

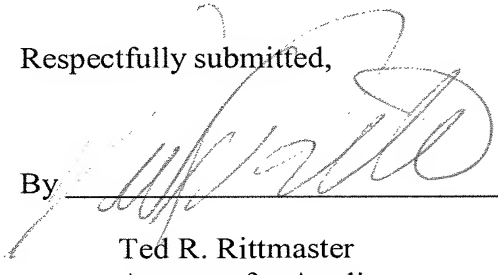
Fees in the amount of \$1,810.00 for payment of the Issue Fee and the Publication Fee are being paid by credit card via EFS-Web.

The Commissioner is hereby authorized to charge any additional fees which may be required regarding this application under 37 C.F.R. §§ 1.16-1.17, or credit any overpayment, to Deposit Account No. 19-0741.

Should no proper payment be enclosed herewith, as by the credit card payment instructions in EFS-Web being incorrect or absent, resulting in a rejected or incorrect credit card transaction, the Commissioner is authorized to charge the unpaid amount to Deposit Account No. 19-0741.

Respectfully submitted,

Date 3-4-11

By 

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